

**Call for Book Chapters/Research Papers for Edited Book**  
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# **Futuristic Trends In VLSI Systems**



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## Call for Chapters

# Futuristic Trends In VLSI Systems

### Aims and Scope:

The aim of this book is to provide an extensive exploration of the emerging trends and innovations at the intersection of Very Large-Scale Integration Systems. This book seeks to be a thorough reference that clarifies the rapidly changing fields of VLSI Systems, including their design, applications, difficulties, future promise, and synergy. The main objective of the book is to provide a tool that encourages scientists, engineers, and students to take advantage of the revolutionary potential of VLSI Systems in the real-time environment. The scope of this book encompasses a series of topics relevant to the exciting and futuristic convergence of VLSI Systems. The book covers a wide range of applications, from energy-efficient electronics systems deployed in various sectors such as smart cities, healthcare, and agriculture, etc.... It also explores the role of the future of VLSI technology with new innovations such as artificial intelligence (AI) and the Internet of Things (IoT) driving the demand for more powerful and energy-efficient chips. Theoretical frameworks, case studies, success stories, and discussions on challenges and future directions provide a holistic view of this dynamic field. Through this broad range of topics, the book seeks to foster understanding, innovation, and collaboration among researchers and professionals in the ever-evolving domain of VLSI Systems.

The book chapters on futuristic trends in VLSI Systems aim to provide a comprehensive overview of the latest advances in this rapidly evolving field. The chapters will cover a wide range of topics, including:

- Digital VLSI system
- Analog VLSI system
- Mixed VLSI system
- Very High-Speed Integrated Circuit Hardware Description Language(VHDL)
- Verilog HDL
- System Verilog
- MOSFET & FINFET
- Low-Power Design
- Leakage estimation and prevention
- Clocking schemes and interconnect
- Algorithm and architecture level optimization
- High performance arithmetic units
- VLSI signal processing
- Advanced memory units
- Reconfigurable and Reprogrammable logic,
- FPGAs
- Neuromorphic Circuits
- Neural Processing Units
- Silicon Photonics
- Nanometer CMOS design

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