

# AN APPROACH OR TORQUE RIPPLE MINIMIZATION IN BLDC MOTOR USING CASCADED H-BRIDGE MULTILEVEL INVERTER

## Abstract

This work proposes a cascaded H-bridge multilevel inverter design for reducing torque ripple in brushless direct current (BLDC) motor drives. Efficiency and affordability are the main challenges for low power motor drives in residential applications. These applications often employ BLDC motor drives because to its high efficiency, minimal maintenance, and simple structure. Due to design and manufacturing constraints, the produced EMF is not perfectly trapezoidal and the current is not inactive. This causes ripples in the load torque, affecting the motor's performance. Using a multilayer inverter reduces the ripple in load torque. The CHB multilevel inverter is used for this purpose and its simulation results are examined in MATLAB/Simulink. The suggested architecture is confirmed using a hardware experimental setup that performs well over a broad range of speeds.

**Keywords:** BLDC Motor, Back EMF, Torque Ripple, Multilevel Inverter Topology

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## I. INTRODUCTION

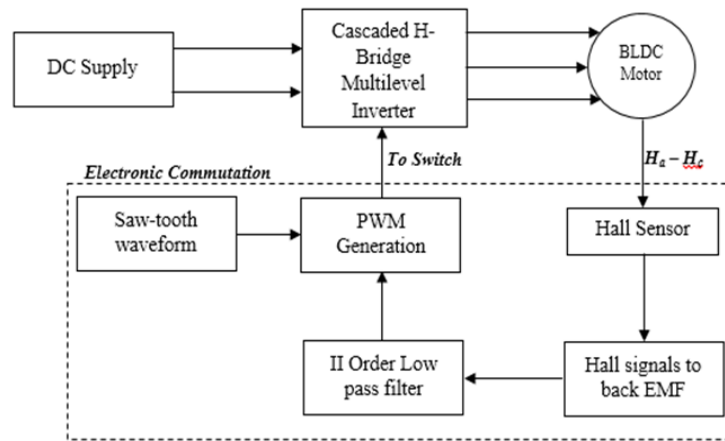
Over the next decade, home appliances like fans and water pumps are predicted to expand at the quickest rate [1-4]. These appliances usually use DC motors. However, lack of maintenance and sparking in the brushes are disadvantages. Consumers now need variable speed motors with great efficiency, cheap cost, and little acoustic noise. These aren't met by conventional motors. Consumers are opting for single phase AC induction motors. These motors are inefficient and run on AC mains at a steady speed. Because of their simplicity, high efficiency, minimal maintenance, high dependability, high flux density per unit volume, and low electromagnetic interference, permanent magnet synchronous motors (PMSM) and brushless DC motors (BLDC) are preferred in these appliances [5-6]. BLDC motors provide better speed control and power density than PMSM motors. Also utilised in robots, medical equipment, precision motion control systems and industrial tools, as well as heating and ventilation systems.

The performance of BLDC motors is vital due to its inherent uses in industries and domestic appliances. The inverter's power electronic switches are commutated using rotor position sensors such hall sensors, optical encoders, or optical resolvers [7-8]. Three square wave current pulses separated by 120° activate the stator windings. The stator windings are activated by the inverter's electronic commutation of power electronic switches. Due to commutation, the resulting load torque has ripples. At any one time, only two phases are active, while the third is inert. The diode freewheeling current causes torque ripple [9]. The ripple in load torque causes acoustic noise in the motor, affecting its performance and speed control characteristics. As a consequence of the non-linearity in the back EMF waveform, conventional control approaches create greater ripple content in the generated load torque [10]. The PWM ON pattern is the optimum option for commutation torque ripple reduction in BLDC motor drives, according to Chuang et al. [11]. Zhang et al. minimised torque ripple in BLDC motor driving by adding a buck converter in front of VSI. Because this approach does not account the buck converter's bandwidth, the drive can only be used at low speeds [12]. For DC link voltage control, Chen et al. used a super lift Luo converter. But the suggested drive performs better at high speeds and the circuit layout is complicated [13].

The efficacy of PWM ON modulation on diode freewheeling torque ripple was studied [14-15]. Fang et al. suggested a unique automated current control approach for BLDC motor drives with non-linear back EMF waveforms. The PWM ON switching pattern decreases diode freewheeling during the inactive period [16]. The decrease of torque ripple has been suggested using artificial intelligence. The flux and torque are calculated using the Clarke and Park transformation. Due to inaccuracy, this transformation takes longer [17-18]. Shakouhi et al. advocated reducing commutation torque ripple by energising all phases just before each conduction period. [19]. However, these topologies increase switching losses and thermal stresses. Multi-Level Inverter supplied BLDC motors provide less distortion in the output load torque. The suggested technique uses a bridgeless topology, specifically a Bridgeless Buck-Boost converter, which has fewer components than existing bridgeless topologies.

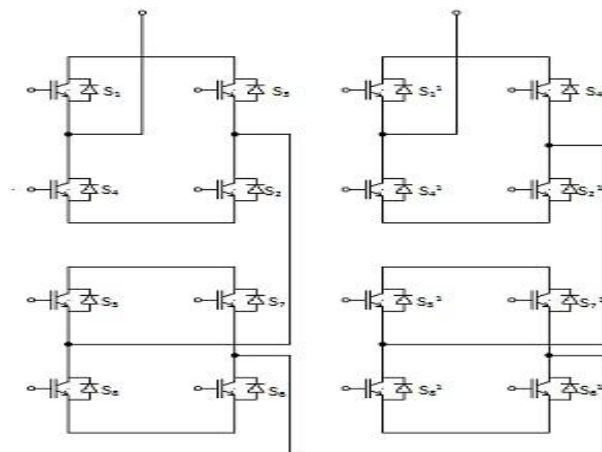
## II. CASCADED H-BRIDGE MULTILEVEL INVERTER

BLDC motors are usually driven by a two-level voltage source inverter. Figure 1 depicts a cascaded H-bridge multilevel inverter supplied BLDC motor. As a consequence of the sharp voltage wavefronts ( $dv/dt$ ) over the terminals of the motors, performance is reduced and motor damage ensues. Multilevel inverters have recently acquired popularity owing to their low  $dv/dt$  ratio, minimal harmonic distortions, low voltage stress on the switches, and low switching frequency. Multilevel inverters are appropriate for high and medium power applications.



**Figure 1:** Block Diagram Representation of Cascaded H-Bridge Multilevel Inverter fed BLDC Motor

The most popular multilevel inverter topologies are diode clamped/neutral clamped, flying capacitor, and cascaded H-bridge. The cascaded H-bridge multilevel inverter requires no clamping diodes or flying capacitors. So it is ideal for BLDC motor drives to reduce torque ripple. Cascaded H-bridge multilevel inverters combine H-bridge inverters with independent DC sources. Three-phase inverters are coupled in a star or delta arrangement. This architecture has fewer components than conventional multilevel inverter topologies, reducing switching loss. Having a separate DC source for each cell restricts its use.  $2(n-1)$  switches per leg for the  $n$ -level inverter Single-phase five-level cascaded inverter topology 2



**Figure 2:** Cascaded H-Bridge Multilevel Inverter

In cascaded H-bridge multilevel inverter each level generates five different output voltages  $+2V_{dc}$ ,  $+V_{dc}$ ,  $0$ ,  $-V_{dc}$ ,  $-2V_{dc}$  by connecting the DC source to the AC output terminal of the inverter with a different combination of four switches. The output voltage of the multilevel inverter is some of the individual H-bridge inverter.

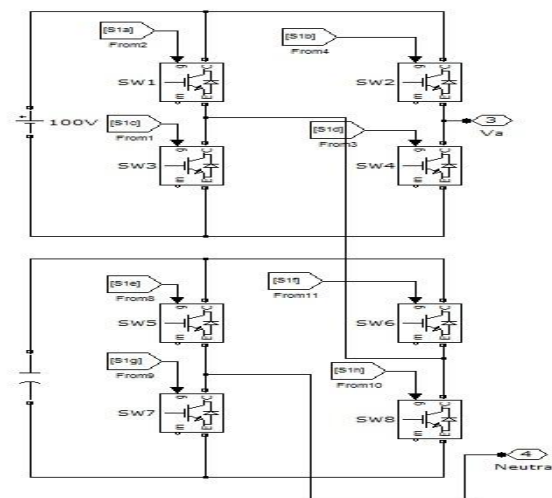
**Table 1: Switching States of Five Level Cascaded H-Bridge Multilevel Inverter**

Switches Turn_ ON	Voltage Level
$S_1, S_2$	$+V_{dc}$
$S_1, S_2, S_5, S_6$	$+2V_{dc}$
$S_4, D_2, S_8, D_6$	$0$
$S_3, S_4$	$-V_{dc}$
$S_3, S_4, S_7, S_8$	$-2V_{dc}$

Table 1 shows a five level inverter's switching table. Four switches are actuated simultaneously, from upper to lower arm. Initially, all upper arm switches are activated, and then all lower arm switches are triggered. As seen in Table 1, five-level cascaded H-bridge multilevel inverters. The output voltage is double the input voltage. So it's ideal for high power applications with low input voltage. Initially, two upper arm switches from the upper cell are conducted, then two upper arm switches and two lower arm switches. At the highest output voltage, four switches are run. A whole cycle has five switching states.

### III. DESIGN OF SIMULATION DIAGRAM

BLDC motors are available in single, two, and three phase. The stator has the same number of windings as the rotor and the permanent magnet. It lacks commutation brushes, as the name implies. Power electronic switches are used for commutation. The electronic commutated motor is thus termed. The switches are commutated by the rotor position. BLDC motors are often driven by two-level inverters, resulting in electromagnetic torque ripple. People nowadays seek low electromagnetic torque ripple; which multilayer inverters may provide.



**Figure 3: Simulink Model for Single Leg of Cascaded H-Bridge Multilevel Inverter**

The simulation model of cascaded H-bridge multilevel inverter is created in MATLAB/Simulink. Three legs with 24 switches form a 3-phase arrangement. Each leg has two cells, each with a DC source. There are 6 isolated DC sources. Figure 3 shows a MATLAB model of a single leg. Figure 4 depicts a three-phase five-level cascaded H-bridge multilevel inverter. Instead of several DC sources, the suggested model features a single DC source with multiple DC links.

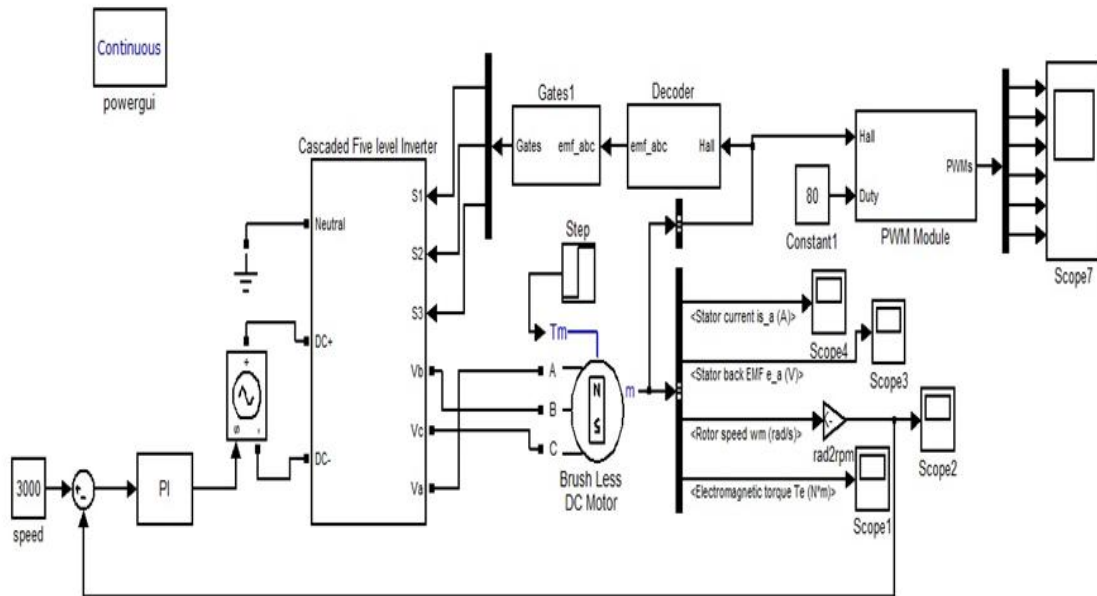


Figure 4: Simulink Model of Five Level Cascaded H-Bridge Multilevel Inverter

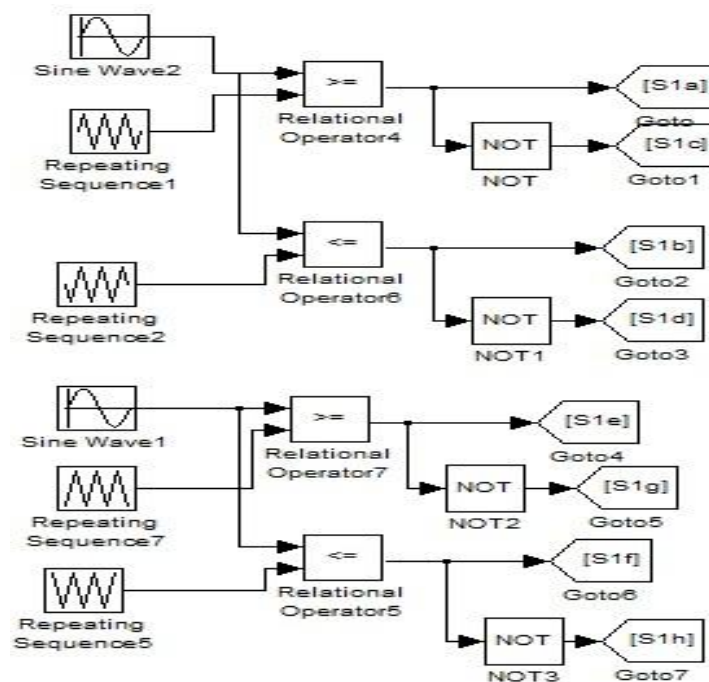
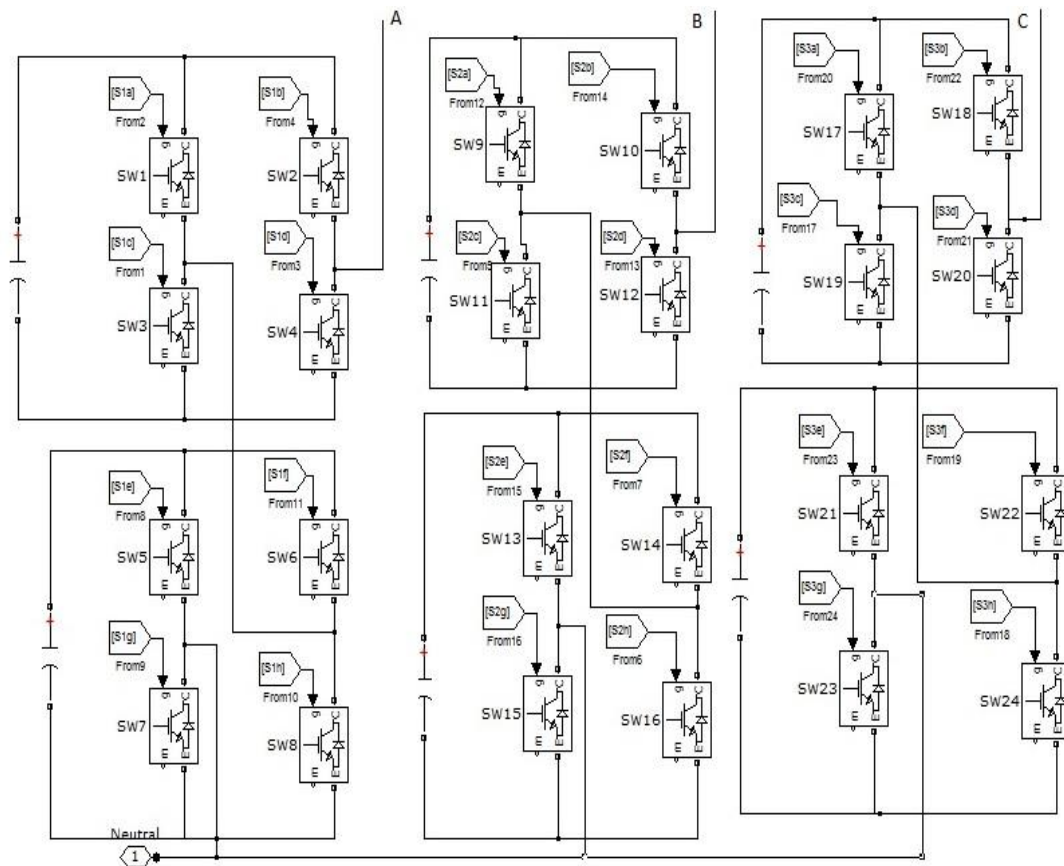


Figure 5: PWM Generation

The initial cell was powered only by DC. The rest of the cells are cascaded or powered by capacitors. The switch firing pulses are created using sinusoidal pulse width modulation. Each leg's sine wave is 1200 phase shifted. To create a pulse, the carrier signal is compared to the reference. Figure 5 shows the PWM generating block. The reverse EMF waveform generates the multilayer inverter firing pulse. The trapezoidal back EMF is sinusoidalized and compared to a reference signal for firing pulse production.

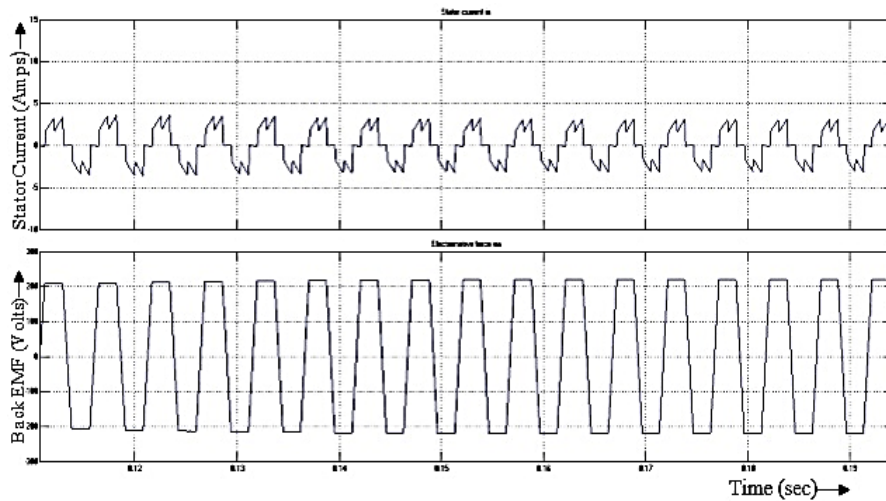


**Figure 6:** Subsystem Block of Five Level Cascaded H-Bridge Multilevel Inverter

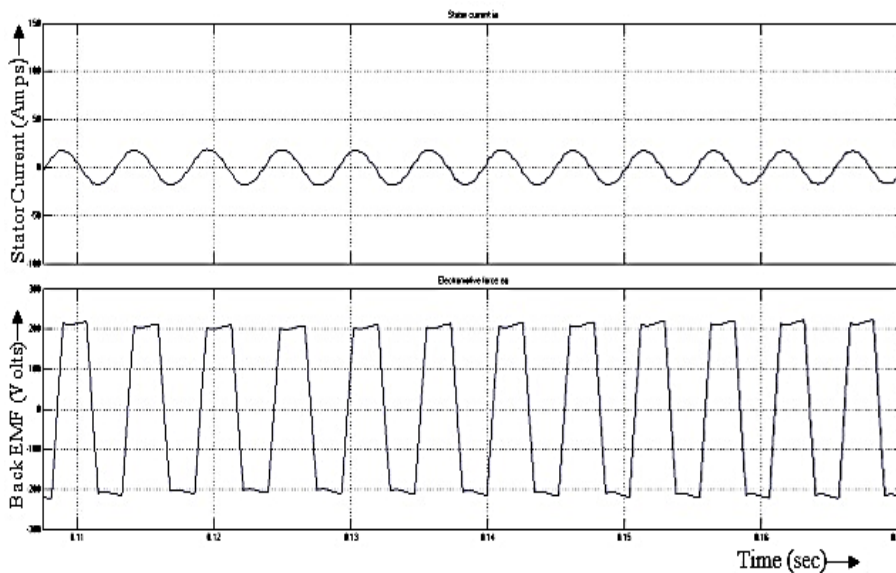
Figure 6 shows the subsystem block of five level Cascaded H-Bridge multilevel inverter. The speed control can be achieved with the help of PI controller. The actual speed of the motor is compared with reference speed value and the resulted error signal is given to PI controller. The PI controller controls the DC link voltage of the inverter depending upon the error value. So speed control can be achieved with different load conditions.

#### IV. SIMULATED PERFORMANCE OF PROPOSED BLDC MOTOR DRIVE

The suggested motor drive's performance is evaluated in terms of stator current, back EMF, electromagnetic torque, and speed. Figure 7 shows the stator current and back EMF waveforms of two and five level H-Bridge Multilevel Inverter supplied BLDC motors. The stator current is sinusoidal owing to the low harmonic content of MLI output. The stator current is distorted in two-level inverter BLDC motors. The back EMF waveform should have little distortion and non-linearity.



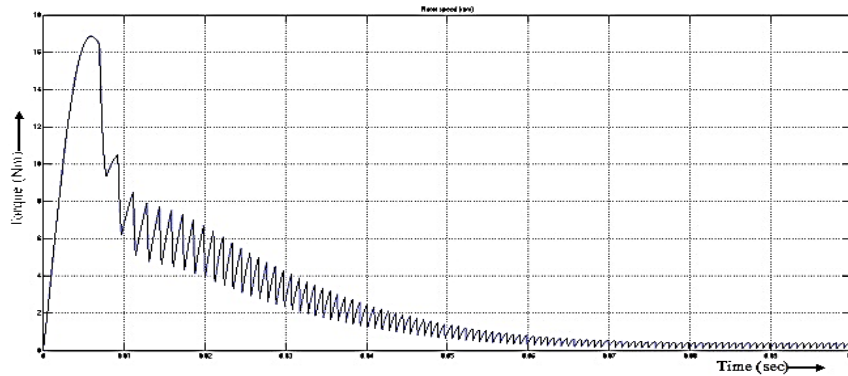
(a) Two Level Inverter fed BLDC Motor



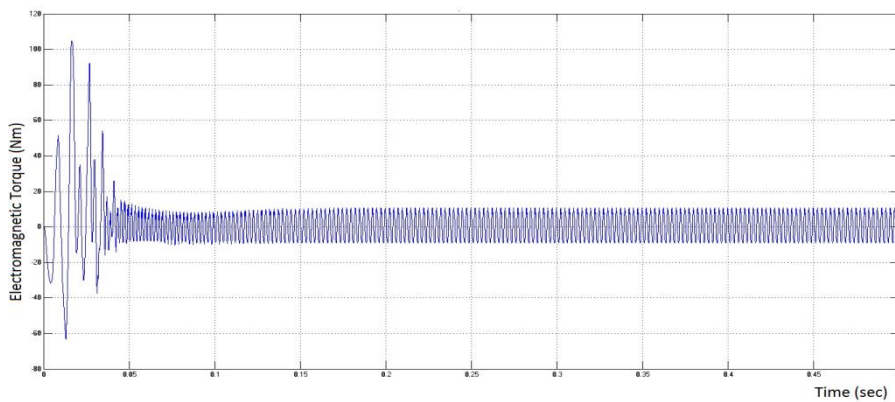
(b) Five Level Cascaded H-Bridge Multilevel Inverter fed BLDC Motor

**Figure 7:** Stator Current and Back EMF Waveform

Due to commutation electromagnetic torque ripple, the generated back EMF is not perfectly trapezoidal in two-level inverter supplied BLDC motors. However, multilevel inverter driven BLDC motors emit a trapezoidal back EMF. This reduces electromagnetic torque ripples and improves overall system performance. A two and five level Cascaded H-Bridge MultiLevel Inverter supplied BLDC motor's electromagnetic torque waveform is depicted in Figure 8.



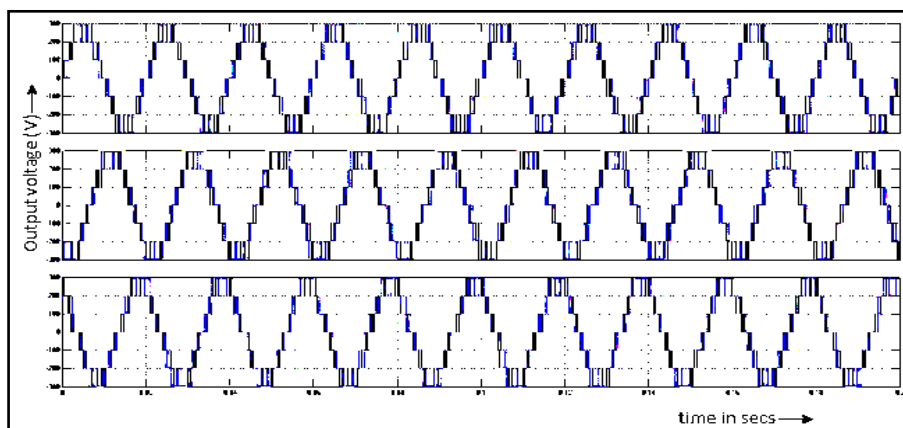
(a) Two Level Inverter fed BLDC Motor



(b) Five Level Cascaded H-Bridge Multilevel Inverter fed BLDC Motor

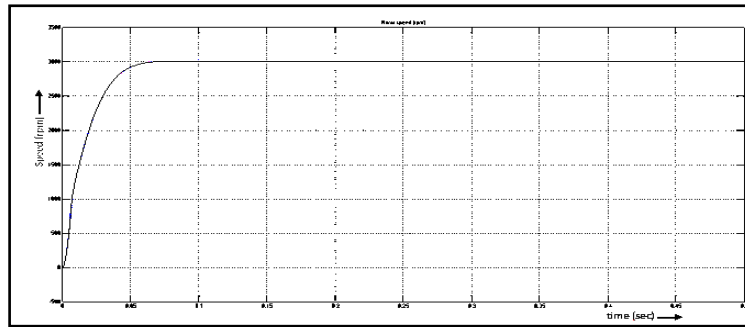
**Figure 8:** Electromagnetic Torque Waveform

When beginning, the torque should be maximal, and then settle to its nominal amount as the speed increases. An inverter's five-level output voltage waveform is displayed in Figure 9. Figure 10 shows the motor's speed response. Due to the regulated source, speed should be linearly modulated.

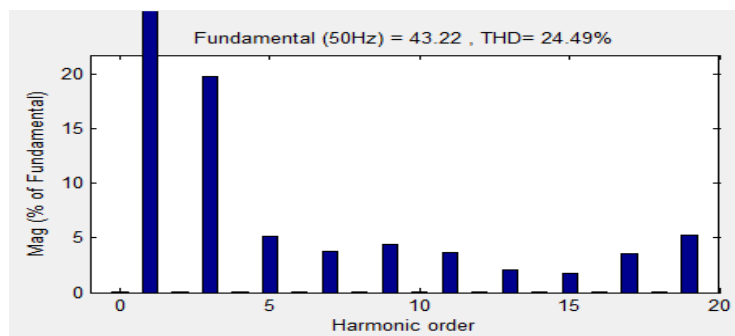


**Figure 9:** Five Level Output Voltage Waveform of CHBML Inverter





**Figure 10:** Speed Response of the Proposed Drive

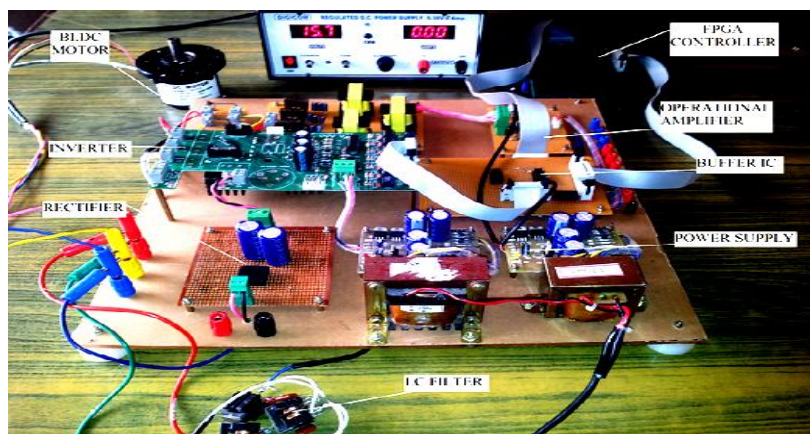


**Figure 11:** FFT Spectrum of Stator Back EMF

Figure 11 displays the stator EMF FFT spectrum. Five-level multilevel inverter fed BLDC motor has lower THD than two-level inverter fed BLDC motor, increasing motor efficiency.

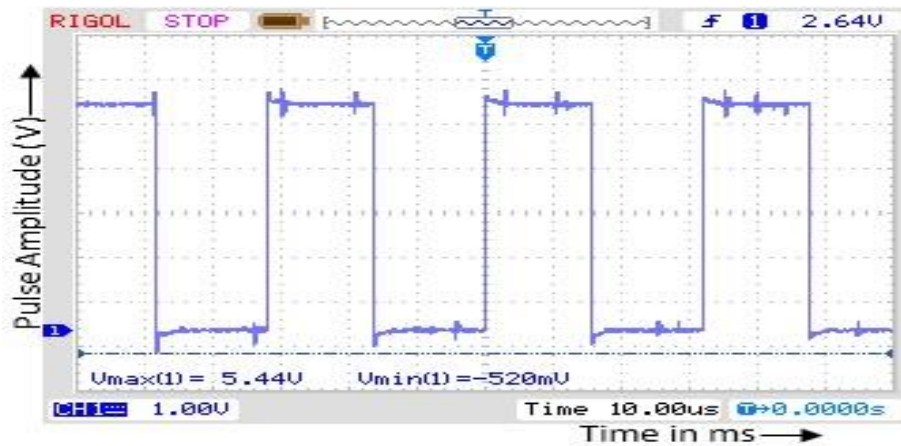
## V. EXPERIMENTAL RESULTS

Figure 12 shows the implementation of a hardware prototype model for proposed cascaded H-bridge multilevel inverter fed BLDC motor drive with various components such as the power supply, driver circuit, relay circuit and PWM generator.

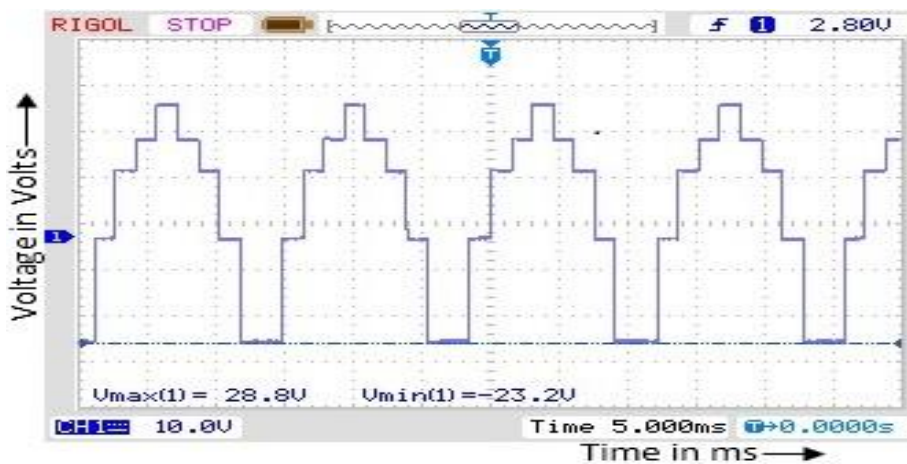


**Figure 12:** Experimental Setup

The driver circuit software is written in embedded C and executed by the FPGA controller. The pulse may be changed by changing the BLDC motor's speed. Upper and lower switches have variable pulse width. Figure 13 depicts the lower inverter switch input pulse. A multilayer inverter has three bridges, each with four switches. a driver circuit with 6 switches Two driver circuit switches and two bridge circuit switches are used for one commutation sequence.



**Figure 13:** Driver Circuit Voltage Waveform for Phase A



**Figure 14:** Five Level Output Voltage Waveform

A multilayer inverter powers the BLDC motor. Two switches in the driving circuit are commutated. The driving circuit's output is square. A staircase waveform requires commutating one H-bridge. So six switches are activated at once, generating a three-level stairwell voltage waveform. There are 6 commutation sequences and 3 H-bridges. Three independent DC sources, capacitor banks, SMPS, and a 12V power supply, power the three bridges. Figure 14 shows the three-level staircase output voltage waveform in steady state. Table 2 summarises the proposed system's overall performance. The table showed that the BLDC motor receives distortionless sinusoidal current. So the motor's efficiency improves. The electromagnetic torque produced is smooth, improving the motor's performance. The response time is excellent.

**Table 2: Result Analysis of CHBMLI Fed BLDC Motor with Two-Level Inverter Fed BLDC Motor**

Characteristics	Two Level Inverter fed BLDC Motor	Multilevel Inverter fed BLDC Motor
Stator Current	Trapezoidal shape and distortion is high (2.6A)	Sinusoidal shape distortion less waveform (4A)
Back EMF	Trapezoidal shape back EMF waveform with minimum distortion (210V)	The shape of the back EMF waveform should be ideal (220V)
Electromagnetic Torque	The ripple in the generated electromagnetic torque is high	Compare to two-level inverter, the ripple content in the torque is minimum
Speed	Linearly varied and settled down slowly to the reference speed (0.15ms)	The starting speed is high with minimum oscillation occurs (0.08ms)
THD	54.61%	24.49%
Power Factor	0.9	0.98

## VI. CONCLUSION

The cascaded H-bridge multilevel inverter fed BLDC motor drive is suggested in this study. It provides sinusoidal stator current with little distortion and trapezoidal back EMF waveform compared to two-level inverter. So, the THD value of these parameters is minimal. As seen above, THD decreases by nearly 60%, resulting in increased efficiency. The PMSBLDCM system has better power quality at AC mains with little torque ripple in a broad speed range. An experimental prototype of the suggested BLDC motor drive with better power quality has been created. The suggested technique performs well and is recommended for low power BLDC motor drives. For household applications, BLDC motors with cascaded H-bridge Multilevel Inverters are the most cost-effective alternative.

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