

Faculty Profile

1. Name: **Dr. Pravin. C. Latane**
2. Designation: Assistant Professor, IT department,
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4. Mobile: 9423166094
5. Date of Birth: 01/05/1974
6. Address: Department of IT, Sinhgad Institute of Technology,
Kusgaon(BK), Lonavala, Pune. 401401
(M.S.) India.
7. Research Interests: **Embedded System, Wireless sensor Networks,
Control and Automation, Internet of Things, Data Science, Antenna Design.**



Educational Details:

- **PhD** (Electronics and Communication), SGVU, Jaipur, Rajasthan, 2023.
- **M.E.** (Electronics), from Govt. COE, Aurangabad, First class, Jan 2010.
- **B.E.** (Electronics) from WCE, Sangli, First class, June 1997.
- **H.S.C.** from SBVE, Atpadi, First class with Distinction, June 1993.

Experience: (2&1/2 years Industrial and 22 years Teaching)

- July 2001-Dec 2001:- Lecturer in COE, Pandharpur. , MS .India
- Jan 2002-Aug 2003:- Lecturer in BV COE, Dhankavadi, Pune. , MS .India
- Aug 2003-Sep 2004:- Lecturer in COE, Malegaon, Baramati. , MS .India
- Sep 2004-June 2010:- Lecturer in COE, Kopergaon. , MS .India
- June 2010-July 2013:- Assistant Professor in ICOER, Wagholi, Pune. , MS .India
- July 2013-till date:- Assistant Professor in SIT, Lonavala, Pune. , MS .India

Professional Membership:

- ISTE (LM31916)
- IEEE (96135661)

Responsibilities:

- Organized Two Days National Level Workshop on "IOT: Transformation in Engineering Education for Digital India" (Jan 2019)
- "Drishti" College Magazine Coordinator
- NBA/ NAAC Dept. Coordinator (till 2019)
- Tectonic 2015, 2016, 2017, 2018, 2019 Dept. Coordinator
- UG and PG Project Guide
- "YUKTI-NIR", IIC Coordinator from AY 2022-23
- Senior Supervisor from AY 2022-23

Research publications:

- International Journals: **18**, International Conference: **11**, National Conference: **03**
- Google Citation :**10**, h-index: **02**, i10 index: **0** (Since 2017)

Books Published:

- **"The Internet of Things Connecting Everything- Introduction, Sensor, Protocol, Application"**, at LAP Lambert Academic Publishing, ISBN: 978-620-5-50976-0, on 19 Oct. 2022.

Copyright and Patent Published:

- Filed copyright of antenna design on **"Design of 4 port microstrip antenna array"** having Diary Number- 17922/2022-CO/L on 26/08/2022 and is under status of "Registered", ROC No -L-119885/2022 on 21 Dec. 2022.

- Published Patent of antenna design on “**Design of 4 Port MIMO Antenna Array for 5G Application**” having Patent Application Number - 202321025187 on 03/04/2023 at 2.29 pm and is under status of “Published” on 05/05/2023.

Achievements and Awards:

- Received Certificate of Appreciation for being **Resource Person** and Guiding Participant in Online Faculty Orientation Program at SPPU on the new curriculum of TE IT (2019 Course) for the subject “Internet of Things and Laboratory Practice-I (IoT)” held on 31st July 2021.
- Received Letter of Appreciation for being **Resource Person** and Guiding Participant “Spreading brightness through sustainability solar lamp and awareness training for rural” on 29th Dec. 2020.
- Appointed as “**Paper Setter & Examiner** for subject Microcontrollers TE(2015)(Insem)” by Board of study in Electronics & Telecommunication, SPPU.
- Appointed as “**Paper Setter & Examiner** for subject Microcontrollers TE(2015)(Endsem)” by Board of study in Electronics & Telecommunication, SPPU.
- Appointed as “**Paper Setter & Examiner** for subject Embedded System and RTOS BE(2015)(Endsem)” by Board of study in Electronics & Telecommunication, SPPU.
- Successfully completed **pre Synopsis DRC of PhD** at SGVU, Jaipur, Rajasthan in June 2021.
- Appointed **subject expert** for finalizing the revised Syllabus of TE IT (2019 Pattern) of AY- 2021-22 for the subject “Elective-I-Internet of Things” by Board of study in Information Technology, SPPU.
- Appointed **subject expert** for finalizing the revised Syllabus of TE E&Tc (2019 Pattern) of AY- 2021-22 for the subject “Elective-III-Embedded System and RTOS”, by Board of study in Electronics & Telecommunication, SPPU.
- Completed **8 Week NPTEL course** on “Evolution of Air Interface towards 5G” (Jan-Apr 2020 semester)
- Received **Letter of Appreciation** in Sem-I(2019-20) for SPPU Final Year E&TC result at STES Level of IOT(100%)
- Received **Letter of Appreciation** in Sem-II(2019-20) for SPPU Third Year E&TC result at STES Level of Mini Project (100%)
- Received **Letter of Appreciation** in Sem-I(2017-18) for SPPU ME E&TC result at STES Level of ESD (100%)
- Published **study material** of Subject “Internet of Things” for BE (E&Tc) students on vselfLearn Engineering online Platform.
- Received **SPPU grant** of RS-2,50,000/- for Two Days National Level Workshop On “IoT: Transformation in Engineering Education for Digital India”.
- Received **Letter of Appreciation** in Sem-I(2016-17) for SPPU ME E&TC result at STES Level of ESD (100%)
- Received **Letter of Appreciation** in Sem-II(2016-17) for SPPU ME E&TC result at STES Level of ESP (100%)
- Received **Letter of Appreciation** in Sem-II(2016-17) for SPPU BE E&TC result at STES Level of BCS (100%)
- Worked as **Co-chair in cPGCON 2018** in SIT, Lonavala, Pune
- Received Certificate of Appreciation for being **Resource Person** and Guiding Participant in Online Faculty Orientation Program at SPPU on the new curriculum of TE IT (2019 Course) for the subject “Internet of Things and Laboratory Practice-I (IoT)” held on 31st July 2021.
- Appointed as “**Paper Setter & Examiner** for subject Elective-I-Internet of Things TE(2019)(Insem)” of AY- 2021-22 by Board of study in Information Technology, SPPU.
- Appointed as “**Paper Setter & Examiner** for subject Digital Circuits SE(2019)(Insem)” of AY- 2021-22 by Board of study in Electronics & Telecommunication, SPPU.
- Appointed as “**Paper Setter & Examiner** for subject Elective-I-Internet of Things TE(2019)(Endsem)” of AY- 2021-22 by Board of study in Information Technology, SPPU.

- Appointed as “**Paper Setter & Examiner**” for subject “Embedded System and RTOS BE(2015)(Endsem)” of AY- 2021-22 by Board of study in Electronics & Telecommunication, SPPU.
- Appointed as “**Syllabus setter and Team Leader**” for subject Embedded System and RTOS BE(2019)” of AY- 2021-22 by Board of study in Electronics & Telecommunication, SPPU.
- Recognized as “**Innovation Ambassador**” at MoE’s Innovation cell and AICTE, IIC.
- Appointed as “**Judge for Project Competition**” on 24/04/2023 at SKNSIT, Lonavala Department of Information Technology, SKNSIT, Lonavala.
- Appointed as “**Judge for Project Competition**” on 04/05/2023 at SIT, Lonavala Department of Electronics and Telecommunication, SIT, Lonavala.
- Completed final **defense of PhD viva-voice** titled “Design and Analysis of MIMO antenna array for 5G application” at Suresh Gyan Vihar University, Jaipur, Rajasthan on 28/06/2023.



Place: Lonavala

Mr. Pravin. C. Latane

Important Link:

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Google Scholar: https://scholar.google.com/scholar?hl=en&as_sdt=0%2C5&q=pravin+latane&btnG=