

Bio Data

Dr. MAHENDER VESHALA

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ACADEMIC PROFILE:

Educational Qualification	Board/University	Name of the Institute	Year of Passing	% of Marks
Ph.D. (Electronics & Communication Engineering)	Kakatiya University	Kakatiya University	2022	-
M.TECH (VLSI Design & Automation Techniques)	NIT	NIT HAMIRPUR	2012	8.27 (CGPA)
B.TECH (Electronics & Communication Engineering)	JNTU-H	SR Engineering College	2009	72.88%
INTERMEDIATE	Board of Intermediate Education, A.P.	Siddhartha Junior College	2005	95.7%
S.S.C.	Board of Secondary Education, A.P.	Sri Vivekananda High School	2003	80.16%

☛ Qualified **UGC-NET** (National Eligibility Test) **JRF** held in December 2011.

☛ Qualified **GATE** three times with more than 98 percentile.

EXPERIENCE:

- ☛ Working as Asst Prof in KU College of Engineering & Technology since 20th September 2013.
- ☛ Working as Academic Consultant in Kakatiya university college of Engineering and Technology in the department of ECE from 18th June 2012 to 19th September 2013.

Positions Held:

Organization	Designation	From- to
KU College of Engineering & Technology(KUCET), KU Campus, Warangal	In-charge Head of ECE Department	Feb,2014-May,2015 & June2018 –May,2020
KUCET,KU Campus, Warangal	In-charge Training & Placement Officer	Aug,2014- Apr,2019
Kakatiya University	Deputy Director for Career Counselling Cell	Mar,2016-March,2017
Ganapathi Deva I,II,III Hostels, KU	Joint Director	Sept,2015 -March, 2017
University College of Engineering &Technology for Women(UCET&W), KU Campus, Warangal	Departmental Committee member-convener of Engineering branches.	20-11-2020 to 19-11-2021
KUCET Boys' Hostel, KU	Joint Director	Apr,2017-Apr,2021
KU College of Engineering & Technology, KU Campus, Warangal	Academic Coordinator	2014-2017
		08-08-2022 to present
	In charge Head of CSE, IT & EEE Departments	01-07-2017 to 30-06-2018 01-07-2019 to 30-06-2020 27-09-2021 to 22-10-2021
	In charge, Examinations branch, KUCET	01-07-2017 to 30-06-2018 01-07-2018 to 21-08-2022
	Vice-Principal	22-07-2017 to 21-07-2018
		22-07-2018 to 21-07-2019
		16-08-2022 to present
	In charge Head of ECE, MECH & CIVIL Departments	01-07-2018 to 30-06-2019
	In charge Head of CSE, & IT Departments	23-10-2021 to 29-11-2022.
	NCC officer	04-05-2016 to 2018
M. Tech Coordinator	24-10-2013 TO 17-02-2014	
Chairmen, Alumina Association	23-08-2014 TO 14-03-2017	
Kakatiya university	BOS Member of ECE	22-05-2019 to present

Extra Activities, Competitive and other Exams Attended and Conducted

- 1) Vide KU circular no.629/1/A3/KU/2013, dated: 26-10-2013, I was appointed as a member for Hospitality committee for 29th Inter-University Central Zone Youth Festival during November 18-22, 2013.
- 2) No. SREC/Ex.B/2014, dated 29-10-2014. I was appointed as external Examiner to conduct II B.Tech . I Sem EDC lab at SREC Warangal on 05th and 06th November,2014.
- 3) No. SREC/Ex.B/2014, dated 12-04-2014. I was appointed as external Examiner to conduct II B.Tech . II Sem DE lab at SREC Warangal on 13th, 14th 18th & 19th April, 2014.
- 4) Lr.No.649/E9/PG/KU/2014, dated 26-06-2014. Chief examiner to B.Tech II yr, II-sem regular examinations, 2014 to undertake the valuation process from 13-06-2014 to 28-06-2014.
- 5) Lr.No.656/E9/PG/KU/2014, dated 02-07-2014. Chief examiner to B.Tech I yr, regular examinations, 2014 to undertake the valuation process from 13-07-2014 to 21-07-2014.
- 6) Lr. No. JNTUH/FDRT-2014/OBSERVER/2014, dated 24-05/2014. I was appointed as observer FDRT-2014 held on 25/05/2014.
- 7) I was appointed as Internal Route Officer for FDRT-2014 held on 11-05-2014.
- 8) I was appointed as Internal Route Officer for EAMCET-2014 held on 22-05-2014.
- 9) I was appointed as Regional Co-ordinator for FDRT-2014 held on 14-05-2014.
- 10) Lr.No.3022/E2/KUW/2015, dated 05-03-2015. Observer to conduct BA/B. Com/BBM/B.Sc supplementary examinations-2014 of SDLCE, KU from 07-03-2015 to 13-03-2015.
- 11) Vide KU Lr.No.90/A2/KU/2015/AO-17, dated 11-02-2015. I was appointed as I/C principal to KU College of Engineering and Technology on 27th December 2014 in place of Prof. T. Srinivasulu principal, as he attended IEEE workshop on “Radar Advances in Satellite & Radar Antennas” at Secunderabad.
- 12) Lr. No. JNTUH/SCRTMT-2015/observers/2015, dated 25-05-2015. I was appointed as observer for SCRTMT-2015 held on 28-06-2015.
- 13) Lr.No.726/E9/PG/KU/2015, dated 02-01-2015. Chief examiner to B. Tech III yr 1st sem, regular examinations, 2014 to undertake the valuation process from 02-01-2015 to 13-01-2015.
- 14) Lr.No.759/E9/PG/KU/2015, dated 15-02-2015. Chief examiner to B. Tech I yr, II,III years 2nd sem, supplementary examinations, 2014 to undertake the valuation process from 16-02-2015 to 22-02-2015.
- 15) Lr.No. 18/Test Center/TSPGECET/OU/2016, dated 26-05-2016.Observer for TSPGECET-2016 at LB College, Warangal held on 31-05-2016.

- 16) Lr.No. 20/Test Center/TSPGECET/OU/2016, dated 26-05-2016.Observer for TSPGECET-2016 at LB College, Warangal held on 03-06-2016.
- 17) Lr.No.1617/E9/PG/KU/2016, dated 01-07-2016. Chief examiner to B. Tech II, III & IV years 1st sem, supplementary examinations, 2016-17 to undertake the valuation process from 01-07-2016 to 11-07-2016.
- 18) Lr.No.1641/E9/PG/KU/2016, dated 12-07-2016. Chief examiner to B. Tech II yr, II-sem regular examinations, 2016-17 to undertake the valuation process.
- 19) Lr.No.492/E7/KUW/2017, dated 16-12-2017.Observer to conduct BA/B. Com eligibility test-2017 of SDLCE, KU on 16th & 17th Dec, 2017.
- 20) Vide KU Order no.786/B4/ACAD/KU/2017, dated: 04-07-2017, I was appointed as a member for Hospitality committee & Lunch for KU XXI Convocation.
- 21) Conducted TSLPRB-2017 as Chief Superintendent.
- 22) Vide KU Lr.No.171/A2/KU/2018-345, dated: 03-03-2018. I was appointed as Committee member to identify branches in B. Tech, KUCE&T
- 23) Lr. No. JNTUH/TSEAMCET-2018/observers/2018, dated 02-05-2018. Observer for TSEAMCET-2018 at Jayamukhi Institute of Technological Sciences from 04-05-2018 to 07-05-2018.
- 24) Rc. No. D2/H/2744/2018, dated 08/11/2018. I was appointed as Presiding Officer to conduct General Elections to TSLA 2018.
- 25) Lr. No. JNTUH/TS ECET-2018/observers/2018, dated 08-05-2018. Observer for TS ECET-2018 at KITS warangal on 09-05-2018.
- 26) Lr.No. 6/Test Center/TSPGECET/OU/2018, dated 26-05-2018.Observer for TSPGECET-2018 at Jayamukhi Institute of Technological Sciences held on 30-05-2018 and 31-05-2018.
- 27) Lr.No. 2/Test Center/TSPGECET/OU/2019, dated 23-05-2019.Observer for TSPGECET-2018 at Jayamukhi Institute of Technological Sciences held on 29-05-2019 and 30-05-2019.
- 28) Lr. No. JNTUH/TSEAMCET-2019/observers/2019, dated 06-05-2019. Observer for TSEAMCET-2019 at Vaageswari College of Engineering,Karimnagar from 08-05-2019 to 09-05-2019.
- 29) Vide KU Lr.No.97/A2/KU/2020/SPL-30, dated: 22-02-2020. I was appointed as EC Member for AKUT, KU.
- 30) Advisory committee member for two-day workshop on “Blockchain Technology” organized by Department of CSE &IT, KUCET, Warangal on 06th & 07th March 2020.
- 31) Lr. No.035/TSICET-2021/KU/Observers, dated 16-08-2021. Observer for TSICET-2021 at iON Digital Zone IDZ 1 Mallapur , Hyderabad on 19-08-2021 & 20-08-2021.
- 32) Lr.No. 58 /TSPGECET-2021/OU, dated 05-08-2021.Observer for TSPGECET-2021 at iON Digital Zone IDZ, Warangal held on 12-08-2021.

- 33) Lr.No. 62/TSPGECET-2021/OU, dated 05-08-2021. Observer for TSPGECET-2021 at Jaya Institute of Technology and science for Women, Warangal held on 14-08-2021.
- 34) Lr. No. JNTUH/TSEAMCET-2021/observers/2021, dated 01-08-2021. Observer for TSEAMCET-2021 at KITS Warangal on 04-08-2021.
- 35) Participated as Faculty Co-ordinator in one day Blood- Donation Camp on 24th March 2022 Organized by Red Ribbon Club & KUCET NSS unit-I&II.
- 36) Lr. No. JNTUH/WD Test/observers/2021, dated 29-12-2021. Observer for WD Test-2021 at SVS Group of Institutions, Warangal on 02-01-2022.
- 37) No.726/COEP/KU/2022, dated 05-04-2022. I was appointed as Special Flying Squad for UG Examinations of SDLCE-2021 held from 06-04-2022 to 06-05-2022.
- 38) Conducted KUCET&T Annual Sports and Cultural Meet-2022, Co-ordinator on 27th April, 2022.
- 39) Lr. No. JNTUH/TSEAMCET-2022/observers/2022, dated 11-07-2022. Observer for TSEAMCET-2022 at Jaya Institute of Technology science, Warangal on 18-07-2022 & 19-07-2022.

Research areas:

- Wireless Sensor networks,
- Low Power VLSI Design,
- Modeling & Simulation
- Analog ,digital and mixed signal design

Number of publications: 33

- International Journals : 24
- International conferences : 09
- ➡ Number of International conferences organized: 01
- ➡ Number of Workshops Attended: 20
- ➡ Number of Workshops Organized: 02
- ➡ Number of National Awards received :01

Research Publications

Journals :(2012-2023)

- [1]. Gandhi Sharanya and Mahender Veshala “Adaptive Energy-Efficient based Cluster Head Selection to Enhance Balanced Utilization of Harvested Energy”, International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XV, Issue XI, pp 8-15, September, 2023.
- [2]. Kukatla Ramya and Mahender Veshala “Enhancing Reliability and Availability of Integrated Rivers Stream flow Data using IPFS and Blockchain Technology”,

International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XV, Issue XI, pp 16-22, September, 2023.

- [3]. Soumya Raparathi, and Mahender Veshala “**Deployment of Wireless Sensor Network for Search and Rescue Operations Using Genetic Algorithm**” , International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XV, Issue XI, pp 23-30, September,2023.
- [4]. M. Pratheesh, **Mahender Veshala** and T. Srinivasulu, “**Optimized Energy Management in Multihop WSNs: Dual path Cluster Approach**”, International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XV, Issue XI, pp 51-56, September,2023.
- [5]. Saraswathi Sankati and **Mahender Veshala** “**A Novel Technique for Effective Image Gallery Search using Content Based Image Retrieval System**”, International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XIV, Issue IX, pp 1-11, 2022.
- [6]. Raju Kathula and **Mahender Veshala** “**Study of TCP Protocol in 5G Technologies over Millimetre Wave**”, International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XIV, Issue IX, pp 1-8, 2022.
- [7]. Abhinav Butti and **Mahender Veshala** “**Skin Cancer Detection Using Machine Learning Algorithms**”, Journal of Engineering Sciences, ISSN 0377-9254, Vol. 13, Issue 09, pp 49-57, 2022.
- [8]. **Mahender Veshala** and T. Srinivasulu “**Improving energy efficiency by optimizing CH selection strategy and optimal relay node selection process in WSNs**”, International Journal of Future Generation Communication and Networking, ISSN 2233-7857, Vol. 14, No. 1, pp 2260-2276, 2021.
- [9]. **Mahender Veshala**, T. Srinivasulu and Rajitha Chelakala “**Improved DEEC protocol with hybrid approach on WSN**”, Journal of Engineering sciences, ISSN :0377-9254, Vol. 12, Issue 12, December, 2021.
- [10]. **Mahender Veshala** and Antiy Soumya “**Collaborative Strategy Based Energy Efficient Routing using RPRA**”, International Journal of Psychosocial Rehabilitation, ISSN : 1475-7192 Vol. 25, Issue 03, 2021.
- [11]. **Mahender Veshala**, T. Srinivasulu and Kancharla Indrani “**Lossless Medical Image Compression Using Region Of Interest**”, Anveshana’s International Journal of Research in Engineering and Applied Sciences, vol. 6, issue 11, ISSN :2455-6300, November,2021.
- [12]. **Mahender Veshala**, T. Srinivasulu and Arshiya Rubeena “**Identification Of Coronavirus Disease From Chest X-Ray Pictures Using CNN**”, Anveshana’s International Journal of Research in Engineering and Applied Sciences, vol. 6, issue 11, ISSN :2455-6300, November,2021.
- [13]. **Mahender Veshala** and T. Srinivasulu “**Fuzzy logic based energy balance routing algorithm in wireless sensor**”, Journal of Xidian University, ISSN 1001-2400, Vol. 14, issue 12 pp 162-171, 2020.

- [14]. Rayella Rashmitha and **Mahender Veshala** “**AOMDV for Prevention of Dark Gap Attacks Using Intrusion Detection System**”, Journal of Emerging Technologies and Innovative Research (JETIR), ISSN 2349-5162, Vol. 6, Issue 6, pp 150-158, 2019.
- [15]. B. Priyanka and **Mahender Veshala** “**Fog Removing Method for the Vision Enhancement in Single Image System**”, The International journal of analytical and experimental modal analysis, ISSN 0886-9367, Vol. XI, Issue IX, pp. 3146-3152, September, 2019.
- [16]. M. Shivaranjani and **Mahender Veshala** “**Underwater Ultra Wideband Fingerprinting Based – Localization**”, The International journal of analytical and experimental modal analysis, ISSN 0886-9367, Volume XI, Issue X, pp. 43-54, October, 2019.
- [17]. J. Priyanka and **Mahender Veshala** “**Prototype Wireless Sensor Network for Environmental Monitoring in Greenhouses using Zigbee Technology**”, International Journal of Research and Analytical Reviews (IJRAR), ISSN 2349-5138, Volume 5, Issue 4, pp. 270-274, November, 2018.
- [18]. A. Manoj Kumar and **Mahender Veshala** “**Control Home Appliances over IOT**”, International Journal of Research (IJR), ISSN 2348-6848, Volume 4, Issue 13, pp 230-232, October 2017.
- [19]. **Mahender Veshala** and T. Srinivasulu “**High Computational Speed with an Optimal Utilization of Resources for Forward Error Correcting Decoder**”, International Journal of Emerging Technology and Advanced Engineering, ISSN 2250-2459, Volume 7, Issue 8, pp 314-318, August 2017.
- [20]. G. Vimla Shalini and **Mahender Veshala** “**Auto-Configuration and Wisdom Connection system on IOT Applications**”, International Journal of Advanced Research Trends in Engineering and Technology, ISSN 2394-3777, Volume 4, Issue 9, pp 162-168, April 2017.
- [21]. P. Rohith and **Mahender Veshala** “**Underwater Positioning and Navigation Based on RF Communication**”, International Journal of Advanced Research Trends in Engineering and Technology(IJARTET), ISSN 2394-3777, Volume 4, Issue 17, pp 149-154, March 2017.
- [22]. Sangam Sai Krishna kumar and **Mahender Veshala**, “**An Innovative Piecewise Linear Companding Method for PAPR Reduction and Companding Distortion in OFDM System**”, International Journal of Computer Engineering in Research Trends, ISSN 2349-7084, Volume 3, Issue 8, pp 404-410, August 2016.
- [23]. N. Navya Teja and **Mahender Veshala**, “**A unique Strategy for Swift Generation and Contrast of Applied Feature Vectors**”, International Journal of Computer Engineering in Research Trends, ISSN 2349-7084, Volume 3, Issue 8, pp 431-4435, August 2016.
- [24]. **Mahender Veshala**, Ramchander Jatooth and Kota Rajesh Reddy, “**Reduction of Short-Channel Effects in FINFET**” International Journal of Engineering and Innovative Technology ISSN 2277-3754, Volume 2, Issue 9, pp 118-124, March 2013.

CONFERENCE PAPERS: (2012-2022)

- [1]. Mahender Veshala and T. Srinivasulu, “**Improving the energy efficiency of the WSNs by Optimal relay node selection using modified Gravitational Search Approach**” IEEE 5th Fifth International Conference on Computing Methodologies and Communication (ICCMC 2021) , ISBN 978-0-7381-1204-6, May 2021.
- [2]. Pedada Madhavi, and **Mahender Veshala**, “**Light Fidelity Advanced Alternative for Wireless Communications**”, International conference on Photonics, VLSI and signal processing, pp 585-588, 2014.
- [3]. **Mahender Veshala**, and Srinivasulu Tadisetty, “**Advanced LED Packages**”, International conference on Photonics, VLSI and signal processing, , pp 456-461, 2014.
- [4]. Muthyala Nakshthra, and **Mahender Veshala**, “**Cardiac Disease Recognition Using Timbral Feature Vector**”, International conference on Photonics, VLSI and signal processing, pp 5256-530, 2014.
- [5]. **Mahender Veshala**, Thulsagari Padmaja and Karthik Ghanta, “**FPGA Based Design and Implementation of Modified Viterbi Decoder for a Wi-Fi Receiver**”, IEEE Conference on Information and Communication Technologies, pp 525-529, 2013.
- [6]. **Mahender Veshala**, Ramchander Jatooth and Kota Rajesh Reddy, “**Diminished Offset Current Analysis in FINFET**”, IEEE Conference on Microelectronics, Communication and Renewable Energy , 2013.
- [7]. **Mahender Veshala**, Balakrishna J and Gagnesh Kumar, “**VLSI Based Design and Implementation of Modified Viterbi Algorithm for a Wi-Fi Receiver**”, International Conference on Recent Trends of Computer Technology in Academia, pp 426-430, 2012.
- [8]. Balakrishna Jerripothula, **Mahender Veshala** and Gagnesh Kumar, “**Performance Comparison on Various Cryptographic Techniques**”, International Conference on Recent Trends of Computer Technology in Academia, pp 526-530, 2012.
- [9]. **Mahender Veshala**, Balakrishna J and Gagnesh Kumar, “**An Optimal Utilization of Resources for Forward Error Correcting Decoder Using BCH Codes**”, International Conference on Computers and Communication, pp 420-424, 2012.

ORIENTATION/REFRESHER COURSES – COMPLETED

S.No.	Title of the Course	Details		
		from	To	Results
1	“47 th Orientation rogramme” at UGC HRDC-JNTUH	08-05-2017	03-06-2017	Grade “A”
2	Refresher course in Material Science at UGC HRDC-UOH	17-01-2019	06-02-2019	Grade “A”
3	Refresher Course on "Internet of Things" at UGC HRDC-JNTUH	09/08/2021	24/08/2021	Grade “A+”

Books and Book Chapters:

- ☀ Contributed as a reviewer of the translation of the book **Fundamentals of Electrical and Electronics Engineering (with Lab Manual)** of 1st year Diploma program in Telugu under AICTE Technical Book Writing Scheme as per the requirement of National Education Policy-2020.

Workshops and FDPs' Conducted

- ☘ Served as a convener to National level one week FDP on **“SCILAB TO ENGINEERING APPLICATIONS”** from 27th July 2020 to 01st Aug 2020 organized by University College of Engineering & Technology for women, Kakatiya University, Warangal.
- ☘ Served as a convener to two day National level workshop on **“Modeling software for Machines and structures”** from 04th & 05th February 2020 organized by KU College of Engineering & Technology, Kakatiya University, Warangal.
- ☘ Successfully organized as coordinator to online National Level Online Quiz On **Covid-19** awareness program from 05/06/2020 to 10/06/2020.
- ☘ Served as convener for two day National Level online training Program on **“Campus Recruitment Training”** Organized by UCETW, KU, Warangal, from 16th -17th August 2020.
- ☘ Organized as Co-ordinator for online Quiz on **“Engineering and Technology”** Organized by UCETW, Kakatiya University, and Warangal on 15th September 2020, on the occasion of Engineer's Day-2020.
- ☘ Served as Co-ordinator in the National Level Online Quiz on **“Machine Learning”** organized by UCETW, KU Warangal, from 03/06/2020 to 06/06/2020.
- ☘ Served as Co- Convener and Faculty Coordinator for Two Day National Workshop on **“PCB Design and Fabrication”** Organized Organized by Department of E.C.E, KUCET in February, 2018.
- ☘ Served as Faculty Coordinator for **“Swachh Brarat programme”** Organized by KUCET, NSS unit I & II at Kakatiya University in August, 2017.
- ☘ Served as Faculty Coordinator for **“Defence Electronics”** Workshop Organized by Department of E.C. E, KUCET in February, 2014.
- ☘ Received certificate of appreciation from Accendere knowledge Management Services Pvt. Ltd for treasured contributions in the implementation of conceptual Research internship Curriculum for REC innovation platform.

Conference Organized

- Convener and one of the Editorial member for International Conference on **Photonics, VLSI and Signal processing(ICPVS)**, which was held at KU College of Engineering & Technology in 28-29th, March 2014.

WORKSHOPS, FDPs' Attended

- Participated in two weeks online faculty development program on “**Hands on Project-Based Approach for Biomedical Signal Analysis using MATLAB**” Sponsored by AICTE and organized by Dept. of ECE, KITW from 01.02.2021-13.02.2021.
- Participated in One-Week Online UGC - Sponsored Short Term Course on "**Cyber Security & Forensics**" held from 26/07/2021 to 31/07/2021 at UGC-HRDC, JNTU, Hyderabad.
- Participated in online Refresher Course on "**Internet of Things**" held from 09/08/2021 to 24/08/2021 at UGC-HRDC, JNTU, and Hyderabad.
- Participated in Five-day National Level Workshop on “**IPR- Patents & Designs filing Process and Research Process and Ethics**” in association with Rajiv Gandhi National Institute of Intellectual Property Management and IPR Cell Sree Vidyanikethan Engineering College from 6th December to 11th December 2021 organized by the Dept. of EEE, Sree Vidyanikethan Engineering College (Autonomous), Tirupati.
- Participated in one week AICTE-ISTE Sponsored Refresher Program on “**Effective Teaching Skills for Outcome Based Engineering Education**” from 14-07-2021 to 20-07-2021, Organized by Department of Electronics and Communication Engineering, S R Engineering College, Warangal.
- Attended the Faculty Awareness Programme on “**Outcome Based Education (OBE) and NBA Accreditation**” organized by Sinhgad Institute of Technology and Science, Narhe, Pune on 17-5-2020.
- Participated in National Level Webinar on “**Application of Tools and Techniques in Research Methodology, Research Writing and Publication**” held on 20th May, 2020 conducted by IQAC, Villa Marie Degree College for Women.
- Participated in “**LaTeX training**” organized at Kakatiya University in January 2020 semester, with course material provided by the Spoken Tutorial Project, IIT Bombay.
- Participated in a 5-Day-National Level Online Faculty Development Program on **ARTIFICIAL INTELLIGENCE** from 22nd May to 26th May 2020 organized by Department of CSE & IT in association with NYCI & Brainovision Solutions India Pvt. Ltd.
- Participated in One Week National Level FDP on **ICT Tools in Education Using MOODLE** conducted from 22-05-2020 to 26-05-2020. Organized by Internal Quality Assurance Cell (IQAC) in Association with Dept. of Computer Science and Engineering, PACE Institute of Technology and Sciences, Ongole.

- Participated Five-Day Online Faculty Development Program (FDP) on “**Professional Enrichment: Role of Soft Skills**” from 24th Aug 2020 to 28th Aug 2020 organized by KU College of Engineering & Technology, Kakatiya University, and Warangal.
- Participated & completed successfully AICTE Training and Learning (ATAL) Academy Online FDP on "**Electrical & Computer Engineering**" from 05-10-2020 to 09-10-2020 at University College of Engineering, Kothagudem, KU.
- Participated in short term training program on “**Hands on Project based approach of 5G design Development**” Sponsored by AICTE and organized by Dept. of ECE, KITW from 02nd -07th Nov 2020.
- Attended the Online Faculty Development Program on **Advancement in Technologies – A Research Perspective**, conducted during 26th – 30th May, 2020, at Anurag University, Hyderabad.
- Participated FDP on “**Network simulation and protocol Analysis using NS-2 NSG-2.1 and wire shark**” organized on 23rd , May 2020 to 25th ,May 2020 by ECE, Narsimha Reddy Engineering College, Hyd.
- Attended one week workshop on “**Research Methodology-RM’19**” organized by KU and KITS, Warangal from 07-11 January 2019.
- Participated Two week FDP on “**Entrepreneurship Development(ED-2019)**” organized by SR Engineering College, Warangal during 18th-30th November 2019.
- Participated a workshop entitled ‘**iB Hubs POWER TO INDIA**’ about “Entrepreneurship & Emerging Technologies” in September,2017.
- Participated AICTE Sponsored Two week faculty development program on “**Hands on Approach of OFDM System Designing in MATLAB**”, organized by the Department of E.C.E. KITS Warangal from 27th November-08th December,2017.
- Attended UGC Sponsored 47th **Orientation program** at JNTU Hyderabad in May/June, 2017.
- Participated faculty Updation program on “**Digital VLSI System Design using Verilog HDL**”, organized by C-DAC, Hyderabad in collaboration with Telangana Academy for Skill and Knowledge (TASK) in January, 2016.
- Participated faculty development program on “**Research Methodology & Computational Techniques (RMCT-16)**”, organized by the Department of Mechanical Engineering, KITS Warangal in JUNE/JULY, 2016.
- Participated ISTE workshop on “**Analog Electronics**”, conducted by IIT Kharagpur, Which was held under the National Mission on Education through ICT (MHRD) in June, 2013.

- Participated AICTE Sponsored faculty development program on “**VLSI Design**”, organized by the Department of E.C.E. in association with ISTE chapter of VNRVJIET & Enhance EDU, IIT Hyderabad at VNR Vignana Jyothi Institute of Engineering and Technology, Hyderabad in May/June, 2013.
- Participated faculty development program on “Advanced Signal processing Techniques in Communications (ASPTC) 2013”, organized by the Department of E.C.E. KITS Warangal in March, 2013.

M.Tech Projects Guided

- ➔ LIGHT FIDELITY-Advanced Alternative for Wireless Communication by student Pedada Madhavi.
- ➔ Cardiac Disease Recognition using Timbral Feature Vector by student Muthyala Nakshatra.
- ➔ An Innovative Piecewise Linear Companding Method for PAPR Reduction and Companding Distortion in OFDM System by student Sangam Sai Krishna Kumar.
- ➔ A Unique Strategy for Swift Generation and Contrast of Applied Feature Vectors by student N. Navya Teja.
- ➔ Underwater Positioning and Navigation Based On RF Communication by student P Rohith
- ➔ Auto-Configuration and Wisdom Connection System on IOT Applications by student G Vimala Shalini.
- ➔ Prototype Wireless Sensor Network for Environmental Monitoring in Greenhouses using Zigbee Technology by student J.Priyanka.
- ➔ Underwater Ultra Wideband Fingerprinting Based – Localization by student M. Shivaranjani
- ➔ Fog Removing Method for the Vision Enhancement in Single Image System by student B. Priyanka.
- ➔ AOMDV for Prevention of Dark Gap Attacks using Intrusion Detection System by student Rayella Rashmitha.
- ➔ Identification of Coronavirus Disease From Chest X-Ray Pictures Using CNN by student Arshiya Rubeena.
- ➔ Lossless Medical Image Compression Using Region of Interest by student Kancharla Indrani.
- ➔ Collaborative Strategy Based Energy Efficient Routing Using RPR by student Antiy Soumya.
- ➔ Improved DEEC Protocol with Hybrid Approach on WSN by student Rajitha Chelakala
- ➔ Analysis of TCP Protocol in 5G Technologies over millimeter wave by student Raju Kathula (20567T6610)
- ➔ Recognition of Indian currency by using MATLAB by student Divya Modem(20567T6613)
- ➔ A Novel Technique for Effective Image Gallery Search using Content Based Image Retrieval System by student Saraswathi Sankati (20567T6616)

B.Tech Projects Guided

1. “Low power VLSI Design using forced sleep method”, April-2016.
2. “Design of carry select adder with low area and power efficiency”April-16
3. “Instant Notification with GPS tracking on vehicle collision and post- crash analysis”, April2017.

4. "Hybrid Drives safety, vigilance and security system for vehicles"
5. "Health monitoring emergency notification and home appliances for multiple disabilities in a person"
6. "Energy efficient data collection in wireless sensor networks"
7. "Smart Home system using wireless sensor networks"
8. "Drowsy driver warning system using python open CV"
9. "Dynamic Effective Distance Cluster Head (DEDCH): Energy Efficient Clustering Algorithm for Wireless Sensor Network ", August-2020
10. "Automated Toll E-Collecting System for Transportation", July2021
11. "IOT Based Smart Parking System" July-2021
12. "Interactive Voice Response and Dual Tone Multi Frequency Based Voting System"July-2022
13. "Third Eye for Blind-Ultrasonic Blind Walking Stick" July-2022
14. "IOT Based Vehicle Accident Detection and Rescue System" April-2023.
15. "Smart Helmet for Safe Driving" April-2023.
16. "Mitigation of Cable bridge Accidents using intelligent devices" April-2023.

PAPER SETTING

- 1) KU,M. Tech. II Semester (Digital Communication) Adaptive Signal Processing (November, 21, 2015)
- 2) KU, M. Tech I Semester(VLSI System Design) Advanced Digital Signal Processing (April 30, 2015)
- 3) KU, M. Tech I Semester(VLSI System Design) Advanced Digital Signal Processing (March 02, 2018)
- 4) KU, M. Tech I Semester (VLSI and Embedded System Design/VLSI System Design) Analog Design (April 01, 2018)
- 5) KU, M. Tech I Semester (VLSI and Embedded System Design/VLSI System Design) Analog Design (July 03, 2021)
- 6) KU,M. Tech. I Semester (Digital Communication) Adaptive Signal Processing (November, 21, 2015)
- 7) KITSW, M. Tech. II Semester(VE) ASIC Design(April 12, 2018)
- 8) KU, B. Tech II Year II Semester (CSE & IT) Basic Electronics (November 16, 2015)
- 9) KU, B. Tech II Year II Semester (CSE & IT) Basic Electronics (April 16, 2019)
- 10) KU, B. Tech II Year II Semester (CSE & IT) Basic Electronics (March 26, 2022)
- 11) KU, B. Tech III Year (ECE) II Semester Digital Communication Systems (April 03, 2022)
- 12) KU, B. Tech II Year(Mining) II Semester Electronics Engineering (April 03, 2022)
- 13) KU, B. Tech III Year I Semester (ECE, E&I & EEE) Linear Integrated Circuits(April 02, 2022)
- 14) KU, B. Tech IV Semester (EEE) Analog Electronics(January 17, 2021)
- 15) KU, B. Tech IV Semester (EEE) Analog Electronics(January 18, 2022)
- 16) KU, B. Tech VII Semester (ECE) Low Power VLSI Design (January 18, 2022)
- 17) KU, B. Tech IV Semester (ECE) Pulse and Integrated Circuits(January 17, 2021)
- 18) KU, B. Tech VI Semester (ECE) VLSI Design(August 31, 2021)
- 19) KU, B. Tech VI Semester (ECE) Wireless Communications (August 31, 2021)
- 20) KU,M. Tech II Semester (Digital Communication) Coding Theory (November 21, 2015)
- 21) KU, B. Tech. III Year II Semester(CSE) Communication System (November 23, 2015)
- 22) KU, B. Tech. III Year II Semester(CSE) Communication System (December 12, 2020)
- 23) KU, B. Tech. III Year I Semester(IT) Communication Theory(November 21, 2015)

- 24) KU, B. Tech. III Year I Semester(IT) Communication Theory(May 01, 2019)
- 25) KU, B. Tech IV Year (ECE) II Semester Data Communication and Networks (April 05, 2018)
- 26) KU, B. Tech IV Year (ECE) II Semester Data Communication and Networks (March 17, 2015)
- 27) KU, B. Tech III Year (ECE) II Semester Digital Communication Systems (March 13, 2015)
- 28) KU, B. Tech III Year (ECE) II Semester Digital Communication Systems (November 10, 2019)
- 29) KU, M. Tech I Semester (Digital Communications) Data Compression Techniques(March 04, 2018)
- 30)) KU, B. Tech III Year (EEE, CSE, IT) I Semester Digital Electronics (May 01, 2019)
- 31) KU, B. Tech II Year (ECE & E&I) I Semester Switching Theory & Logic Design (November 08, 2019)
- 32) KU, M. Tech I Semester (Digital Communication) Detection & Estimation Theory(March 02, 2018)
- 33) KU, M. Tech I Semester (Digital Communication) Detection & Estimation Theory(April 08, 2015)
- 34) KU, M.Tech II Semester(VLSI and Embedded Systems Design/VLSI System Design) Design For Testability(November 27, 2015)
- 35) KU, M.Tech II Semester(VLSI and Embedded Systems Design/VLSI System Design) Design For Testability(January 28, 2021)
- 36) KU, M. Tech I Semester (VLSI and Embedded Systems Design/VLSI System Design) Digital Design (February 10, 2019)
- 37) KU, M. Tech I Semester (CSE) Digital Design (July 04, 2021)
- 38) KU, M. Tech I Semester (VLSI and Embedded Systems Design/VLSI System Design) Digital Design (July 03, 2021)
- 39) KU, B. Tech. IV Year (IT) I Semester Digital Image Processing (March 04, 2021)
- 40) KU, B. Tech IV Year (ECE) II Semester Digital System Design(April 10, 2018)
- 41) Vaagdevi College of Engineering, M.Tech. I Year I Semester Digital System Design(April 23, 2017)
- 42) KU, B. Tech II Year (EI, ECE, EEE) I Semester Electronic Devices & Circuits – I (November 14, 2015)
- 43) KU, B. Tech II Year (EI, ECE, EEE) I Semester Electronic Devices & Circuits – I (April 29, 2019)
- 44) KU, B. Tech II Year (EI, ECE, EEE) II Semester Electronic Devices & Circuits – II (November 17, 2015)
- 45) KU, B. Tech II Year (EI, ECE, EEE) II Semester Electronic Devices & Circuits – II (April 14, 2019)
- 46) KU, B. Tech II Year (Mech. Engg) I Semester Elements of Electronics Engineering (November 15, 2015)
- 47) KU, B. Tech II Year (Mech. Engg) I Semester Elements of Electronics Engineering (May 09, 2019)
- 48) KU, B. Tech IV Year (E&I ,ECE) II Semester Embedded System Design (April 05, 2018)
- 49) KU, B. Tech IV Year (E&I ,ECE) II Semester Embedded System Design (May 01, 2019)
- 50) KU,M. Tech. II Semester (Power Electronics) Embedded systems (August 22, 2018)
- 51) KU, M. Tech I Semester (VLSI and Embedded Systems Design/VLSI System Design) Embedded System Concepts (March 04, 2018)
- 52) KU, M. Tech I Semester (VLSI and Embedded Systems Design/VLSI System Design) Embedded System Concepts (February 09, 2020)
- 53) KU, M. Tech I Semester (Digital Communication) Embedded System Design (February 09, 2020)
- 54) KU, B. Tech IV Year (ECE) I Semester Industrial Electronics (March 03, 2021)
- 55) KU, B. Tech III Year (EI, ECE, EEE) I Semester Linear Integrated Circuits (May 09, 2019)
- 56) KU, B. Tech V SEMESTER (EEE) Linear IC Applications (March 21, 2021)
- 57) KU,M.Tech. II Semester (Digital Communication) Low Power VLSI Design (January 27, 2021)
- 58) KITSW, M. Tech I Semester (Digital Communication) Modern Digital Communication Techniques (December 17, 2017)
- 59) KU, M. Tech (Power Electronics) II-Semester MP&MC(August 22, 2018)
- 60) KU, M. Tech I Semester (Digital Communication) Microwave and Optical Fiber Communication Systems (February 08, 2019)
- 61) KU, B. Tech IV Year (ECE) I Semester Optical and Satellite Communication Systems (June 09, 2017)
- 62) KU, B. Tech III Year (ECE) I Semester Pulse and Digital Circuits (May 06, 2018)

- 63) KITSW, B.Tech. IV Semester(ECE) Probability and Random Processes(February 09, 2016)
- 64) KU, B. Tech II Year (EI, ECE, EEE) I Semester Signals and Systems(November 17, 2017)
- 65) KITSW, B.Tech. III Semester(ECE) Signals and Systems (May 05, 2017)
- 66) Vaagdevi College of Engineering, B. Tech IV Semester (EEE) Analog & Digital Electronics (July 07, 2022)
- 67) Vaagdevi College of Engineering, B. Tech II Year II Semester Computer Organization (October 21, 2020)
- 68) Vaagdevi College of Engineering, M. Tech I Year II Semester Coding Theory and Techniques (October 01, 2015)
- 69) Vaagdevi College of Engineering, M. Tech I Year I Semester Digital System Design (April 05, 2015)
- 70) Vaagdevi College of Engineering , M. Tech. I Year II Semester (VLSI System Design) Digital Signal Processors and Architectures(October 01, 2015)
- 71) Vaagdevi College of Engineering, B. Tech I Year II Semester(EEE) Electrical Circuits – I (October 30, 2020)
- 72) Vaagdevi College of Engineering, B. Tech IV Year I Semester Embedded Systems Design (January 05, 2022)
- 73) Vaagdevi College of Engineering, M.Tech I Year I Semester Image and Video Processing(January 20, 2016)
- 74) Vaagdevi College of Engineering, B. Tech IV Year I Semester Introduction to Microcontroller & Applications (October 22, 2020)
- 75) Vaagdevi College of Engineering , B. Tech III Year II Semester(ECE) Neural Networks & Applications(November 25, 2020)
- 76) Vaagdevi College of Engineering , B. Tech III Year II Semester(ECE) Satellite Communication(November 25, 2020)

Dr. Mahender Veshala