

Resume



- **Name:** Dr. C. V. Guru Rao
- **Academic Qualifications:**

- **Ph.D.** (Doctor of Philosophy) in **Computer Science & Engineering** from **Indian Institute of Technology, Kharagpur**, West Bengal, India in 2004 under **Quality Improvement Program** of All India Council for Technical Education (**AICTE**).
- **M.E.** (Master of Engineering) in **Information Science & Engineering** from **Motilal Nehru Regional Engineering College, Allahabad**, India in 1994 under **Cross Migration Scheme** of Department of Electronics, Govt. of India.
- **M.Tech.** (Master of Technology) in **Electronic Instrumentation** from **Regional Engineering College, Warangal**, Andhra Pradesh, India in 1985.
- **B.Tech.** (Bachelor of Technology) in **Electronics & Communication Engineering** from **V. R. Siddhartha Engineering College, Vijayawada (Nagarjuna University, Guntur)**, India in 1981.

- **Professional Qualification:** *LMISTE, LMISOI, MIE, MIETE, SMIEEE (USA), MIEEE-CS, MCSI, MACM, IMIDES*
- **Subject:** Computer Science and Engineering

Designation:

- Professor of Computer Science and Engineering & Head,
Department of Computer Science and Engineering,
Gayatri Vidya Parishad College of Engineering, Visakhapatnam.

Contact Details:

- **Postal Address:**
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Department of Computer Science and Engineering,
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[cvgururao@gvpce.ac.in\(official\)](mailto:cvgururao@gvpce.ac.in).

- **Contact no:** +91 98496 49069(Personal)

Teaching Experience: 38+ years

- **Past experience:**

Joined as lecturer in Electronics & Instrumentation Engineering at Kakatiya Institute of Technology and Science Warangal on approval of appointment by Kakatiya University by on 16 November, 1985 and continued till the appointment as principal of the institution on 01-08-2007 and served in that capacity until January 31, 2011. Subsequently in

March 2011 joined SR Engineering College, Warangal and worked for securing Autonomous status, Accreditations like NAAC as well as NBA and upgrading it to the University level. Worked as Professor of CSE & Director of Evaluation at SR University, Warangal. Currently, working as Professor & Head at Department of Computer Science & Engineering of Gayatri Vidya Parishad College of Engineering, Visakhapatnam. The details of employment are furnished in the table listed below:

Duration		Designation	Department	Institution
From	To			
July'24	Till Date	Professor of CSE	Computer Science & Engg	GVPCE, Visakhapatnam
June'20	July'24	Professor of CSE & Director of Evaluation	School of CS & AI	SRU, Warangal
June'20	Oct'23	Prof & Dean, SOCS&AI	School of CS & AI	SRU, Warangal
April'17	June'20	Professor of CSE & Director	School of CS & AI	SREC, Warangal
Nov'15	April'17	Professor of CSE & Director of Evaluation	Computer Science & Engg	SREC, Warangal
Oct'13	Oct'15	Principal		SREC, Warangal
Mar'11	Sep'13	Professor & Head	Computer Science & Engg	SREC, Warangal
Feb'11	Mar'11	Professor	Computer Science & Engg	KITS, Warangal
Aug'07	Jan'11	Principal		KITS, Warangal
Jun'04	Jul'07	Professor	Computer Science & Engg	KITS, Warangal
Oct'03	June'04	Assistant Professor	Computer Science & Engg	KITS, Warangal
Sep'00	Oct'03	Assistant Professor (On Deputation)	Computer Science & Engg	IIT, Kharagpur
Aug'95	Sep'00	Assistant Professor	Computer Science & Engg	KITS, Warangal
Jul'94	Aug'95	Senior Lecturer	Electronics & Instn Engg	KITS, Warangal
Jul'92	Apr'94	Lecturer (On Deputation)	Electrical Engg: CSE Program	MNREC, Allahabad
Nov'85	Jul'92	Lecturer	Electronics & Instn Engg	KITS, Warangal

Professional Memberships:

- Life Member, **Indian Society for Technical Education (LM8360)**
- Life Member, **Instrument Society of India (ISOI)**
- Member, **Institution of Engineers (MIE) (M-106044/3)**
- Member, **Institution of Electronics & Telecommunications Engineers (MIETE) (M-90037)**
- Senior Member, **Institution of Electrical & Electronics Engineers (USA)** (Member No. 40318690)
- Member, **Computer Society of India** (Member No N1006267)

- Member, **Institution of Electrical & Electronics Engineers (USA) Computer Society** (Member No 40318690)
- Professional Member, **Association of Computing Machinery (ACM)** (Member No. 5373001).
- Member, Industry Institute Interaction Panel of **Confederation of Indian Industry (CII)**, Andhra Pradesh, 2010.
- Member, **Confederation of Indian Industry (CII)**, TELANGANA, 2015.
- Invited Member, **ACEEE, Institute of Doctors Engineers and Scientists (IDES)** (Member No: 1612).

Research & Publications:

- **Research Guidance: Ph.D. (Awarded: 20, Ongoing: 12), M.Tech(Awarded:17)**
 1. **“A framework for integration and standardization of Data from Heterogeneous Sources”** registered by Mr. C. Sunil Kumar at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) was awarded **Ph.D** on 27.12.2011.
 2. **“An Integrated approach for classifying and retrieval of reusable components”** registered by Mr. P. Niranjana at Kakatiya University, Warangal was awarded **Ph.D** on 16.12.2013.
 3. **“Palmprint Processing, Classification and Identification”** registered by Mr. Dhananjay D. Maktedar at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) was awarded **Ph.D** on 16.05.2015.
 4. **“Efficient Evaluation of Iceberg Queries by Compressed Bitmap Index Technique in Distributed Databases”** registered by Mr. Vuppu Shankar at Kakatiya University, Warangal was awarded **Ph.D** on 09.12.2016.
 5. **“A Strategy for data memory exploration and cache configuration of low power real time embedded systems”** registered by Mrs. C. Srilatha at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and her Ph.D was awarded on 28.01.2017.
 6. **“Multiplicative Data Perturbation for Preserving Privacy”** registered by Ms. Tanveer Jahan at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and her Ph.D was awarded on October 28, 2017.
 7. **“Development of Heuristic Scales for Disease Prediction using Data Mining in Healthcare”**, registered by Mr. N. Ramana at

Kakatiya University, Warangal and his Ph.D was awarded on November 24, 2018.

8. **“A Middleware Modeling for Hybrid Mobile Ad-hoc Networks”** registered by Mrs. PG Sunitha Hiremath at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and her Ph.D was awarded on December 14, 2018.
9. **“Novel Near Duplicate Document Detection Algorithms For Web Applications”** registered by Ms. P. Lavanya at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and her Ph.D was awarded on January 6, 2020.
10. **“Machine Learning Based Models For Detecting Http Input Validation Vulnerabilities of Web Applications”** registered by Mr. S. Venkatramulu at Kakatiya University, Warangal and his Ph.D thesis on was awarded June 6, 2020.
11. **“Secure Key Authentication Scheme Using Discrete Logarithms and Chinese Remainder Theorem”** registered by Sri P. Kumara Swamy at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and his Ph.D was awarded on June 12, 2020.
12. **“Network Coding-Aware Routing Protocols To Enhance Performance of Ad-hoc Networks”** registered by V. Prashanthi at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and her Ph.D was awarded on June 22, 2020.
13. **“Machine Learning Based Approach to Cluster Similar Software components for facilitating effective Software Reuse”** registered by Mr. C. Srinivas at Kakatiya University, Warangal and his Ph.D was awarded on July 24, 2020.
14. **“Improved Bayesian Anti-Spam Filter Implementation and Analysis of Independent Spam Corpuses”** registered by Ms. P U Anitha and was successfully defended Ph.D Viva-Voce at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) on August 08, 2022.
15. **“Controlling IP Forging using Realistic Simulation”** registered by Ms. Madhusri and was successfully defended Ph.D Viva-Voce at Osmania University College of Engineering, Osmania University, Hyderabad on 29.04.2023.
16. **“Security, Privacy and Trust issues of Cloud Computing”** registered by Mr. K. Ramakrishna and was successfully defended Ph.D. Viva-Voce at Osmania University College of Engineering, Osmania University, Hyderabad on 02.02.2023.

17. **“Secure Web Services for Distributed Applications”** registered by Mr. Shyam Prasad and was successfully defended Ph.D. Viva-Voce at Osmania University College of Engineering, Osmania University, Hyderabad on 21.02.2023.
18. **“An AI Enabled Framework for Efficient Early Brain Stroke Detection”** registered by Ms. A. Devaki and was successfully defended Ph.D. Viva-Voce at Osmania University College of Engineering, Osmania University, Hyderabad on 24.06.2023.
19. **“An Improved Optimization of Routing and Energy Conservation in Manets”** registered by Mr. Battula Phijik and was successfully defended Ph.D. Viva-Voce at Osmania University College of Engineering, Osmania University, Hyderabad on 02.02.2023.
20. **“A New Multipath Routing Protocol for Ad-hoc Networks”** registered by Ms. P. Shailaja submitted her thesis in May 2022 at Jawaharlal Nehru Technological University, Hyderabad (JNTUH) and was successfully defended Ph.D. Viva-Voce at Department of Computer Science & Engineering, JNTUH on 16.07.2024.

- **Research Projects:**

1. Worked as principal investigator of Research Promotion Scheme financed by All India Council for Technical Education (AICTE): **“CAT Tool for Mixed-Signal SOC Designs”** sanctioned by AICTE, New Delhi under Research Promotion Scheme at a cost of Rs 4.0 lakhs.
2. Mentored a research project on **“Detecting and Preventing information leakage in software for security and intellectual property protection”** sanctioned to Ms. S. Poornima at a total cost of Rs 20.55 lakhs under the Women Scientist scheme-A (WOS-A) of the Department of Science and Technology, Govt. of India, New Delhi.

- **Papers Published:**

S.No.	Publication	Published
1	Book Chapters	21
2	International Journals	147
3	International Conferences	85
4	National Conferences	19

- **Patents Published: 2**

1. P.Kumaraswamy, Dr.C.V.Guru Rao, **“An Authentication Scheme Using Public Key Cryptography”**, vide application no: 202041000901 dt: 08/01/2021 was published in the patent office journal no: 08/2020 dated: 21/02/2021.
2. Madhavan, Dr. Guru Rao, Dr. Girirajan, Dr. Taher Basha, Ms.V.Savithri Padma Priya, **“An Authentication Scheme Using Public Key Cryptography”**, vide application no: 202341084925 dt: 13/12/2023 was published in the patent office journal no: 02/2024 dated: 12/01/2024.

- **Books Published:**

S.No.	Title	Publisher	Month and Year of Publication	ISSN/ISBN
1	The Design and Analysis of Algorithms	Pearson Education India	2008	9780321358288
2	An Integration and Standardization of data from heterogeneous sources	LAP LAMBERT Academic Publishing GmbH & Co., Germany	2012	978-3-8473-3968-7
3	Cache Configuration for High Performance Embedded Systems	LAP LAMBERT Academic Publishing GmbH & Co., Germany	July 2013	978-3-6593-9232-0
4	A Multiplicative Data Perturbation	LAP LAMBERT Academic Publishing GmbH & Co., Germany	November 2016	978-3-659-97067-2
5	Key Authentication Schemes in Public Key Cryptography	LAP LAMBERT Academic Publishing GmbH & Co., Germany	March 2017	978-3-330-06210-8

Nominations in Different Positions (fully Honorary):

- Advisor, 21st Century Gurukulam, Kakatiya University, Warangal.
- Member, Academic Advisory Board, **Monand University**, New Delhi.
- Member, Board of Studies in Computer Science & Engineering, Information Technology and Computer Applications, **Lakkireddy Balireddy College of Engineering**, Mylavaram, Krishna District, 2010.

- Member, Board of Studies in Computer Science & Engineering and Information Technology, **Kakatiya University**, Warangal.
- Chairperson, Board of Studies in Computer Science & Engineering and Information Technology, **Kakatiya University**, Warangal.
- Chairperson, Board of Studies in Computer Science & Engineering, Information Technology and MCA, SR Engineering College (**Autonomous Under JNTU Hyderabad**), Warangal.
- Member, Academic Council, SR Engineering College (**Autonomous Under JNTU Hyderabad**), Warangal.
- Member, Governing Body, SR Engineering College (**Autonomous Under JNTU Hyderabad**), Warangal.
- Member, Board of Studies in Electronics and Computer Engineering, Jawaharlal Nehru Technological University Anantapur.
- Member, Board of Studies in Computer Science & Engineering, Jawaharlal Nehru Technological University Kukatpally, Hyderabad.
- Member, Board of Studies in Computer Science & AI, SR University Warangal.
- Member, Academic Council, SR University Warangal.
- Member, Board of Management, SR University Warangal.
- Research Supervisor for Kakatiya University, Warangal
- Research Supervisor for Jawaharlal Nehru Technological University (JNTU), Hyderabad
- Research Supervisor for Rayalaseema University, Anantapur.
- Research Supervisor at Osmania University, Hyderabad.
- Editor-in-Chief for the International Journal of Mathematics, Computer Science & Information Technology.
- Member on the Editorial Board of the International Journal of Computational Intelligence Research and Applications.
- Editor for the International Journals of Library Science.
- Member on the Advisory Board of the International Journal of Computational Intelligence Techniques of Bioinfo Publications **ISSN: 0976-0466 (Print); E-ISSN: 0976-0474 (Online)**.
- Member of the Chief Advisory Board of the Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.
- Chief Superintendent of Graduate Aptitude Test for Engineers (GATE)-2010 examination organized by Indian Institute of Technology Madras, Chennai, on 14.2.2010.
- Chief Superintendent of Joint Entrance Examination (JEE)-2010 examination organized by Indian Institute of Technology Madras, Chennai, on 11.4.2010.

- Chief Superintendent of Group-IV examinations conducted by Andhra Pradesh Public Service Commission (APPSC), Government of Andhra Pradesh, on 3.10.2010.
- Chief Superintendent of ICET-2010 conducted by Convener, ICET-2010, Govt. of Andhra Pradesh, Hyderabad on 27.5.2010.
- Chief Superintendent of EAMCET-2010 conducted by Convener, EAMCET-2010, Govt. of Andhra Pradesh, Hyderabad on 30.5.2010.
- Expert Member of National Board of Accreditation Visiting team for Accreditation of B.Tech courses at Erode Sengunthar Engineering College, Erode, Tamil Nadu during March 1-3, 2013.
- Expert Member of National Board of Accreditation Visiting team for Accreditation of B.Tech courses at Paavai Engineering College, Paavai, Tamil Nadu during March 29-31, 2013.
- Expert Member of National Board of Accreditation Visiting team for Accreditation of B.Tech courses at Swami Keshvanand Institute of Technology, Management & Gramothan, Ramnagar (Jagatpura), Jaipur, Rajasthan during April 12-14, 2013.
- Expert Member of National Board of Accreditation Visiting team for Accreditation of B.Tech courses at Madha Engineering College, Chennai, Tamil Nadu during May 2-4, 2013.
- Expert Member of National Board of Accreditation Visiting team for Accreditation of B.Tech courses at PSN College of Engineering & Technology, Tirunelveli, Tamil Nadu during May 24-26, 2013.
- Expert Member of National Board of Accreditation Visiting team for Accreditation of B.Tech courses at ITM University, Gurgaon during July 24-26, 2013.
- Member of Inspection Committee constituted for grant of extension of affiliation to the affiliated Engineering Colleges under Kakatiya University, Warangal during 2009-2010 and 2010-2011.
- Member in Selection Committee constituted for recruitment of faculty in Computer Science & Engineering, under Kakatiya University, Warangal during the years, 2006-2007 and 2009-2010.
- Member, EAMCET-2010 Admissions Committee, for the academic year 2010-2011.
- Member, PGECET-2010 Committee for the academic year 2010-2011.

Other Activities Organized:

- Delivered a lecture on the Topic **“Test & Testability”** in the Short-Term Course on **“VLSI Design”** held at Vignan Engineering College, Vadlamudi, Guntur during June 2004.

- Delivered a lecture on the Topic **“VLSI & Design”** at RVR & JC College of Engineering, Guntur during March 2004.
- Delivered a lecture on **“Reflections on Ragging and Prohibition Act, 1997”** in meeting of the Principals of all colleges, Police Officials in Warangal district, held at ZillParishad Hall, Warangal during August 2009.
- Delivered a lecture on **“Software Management”** at Ramanandatirtha College of Engineering, Nalgonda during September 2010.
- Delivered a lecture on **“Enterprise Computing”** at SR institute of Technology for Women, Warangal during November 26, 2011.
- Delivered an Expert lecture on the Topic **“Introduction to C Programming Language”** at SR Engineering College, Warangal on July 25, 2012 in the APSICHE-JNTUH sponsored one week Staff Development Program on **“C Programming and Data Structures”** held in association with Andhra Pradesh State Council of Higher Education, Hyderabad during July 25-31, 2012.
- Delivered an Expert lecture on the Topic **“Multimedia Information Retrieval”** at Two-day National Level Workshop on **“Information Retrieval Systems”** held at Varadha Reddy College of Engineering, Warangal on January 29, 2013 in association with Department of Science and Technology, New Delhi.
- Delivered a Keynote Address and Expert lecture on **“Cloud Computing and Security issues”** at VNRVJ Institute of Engineering and Technology, Hyderabad on June 03, 2013 in the AICTE Sponsored Two-Week Staff Development Program on **“Recent Trends in Cloud Computing and Security”** held during June 03-15, 2013.
- Delivered an Expert lecture on the Topic **“Secure and Long-term Data Management”** at Three-day Residential Training Program on **“Malicious Software and its underground Economy”** held at Andhra Pradesh Human Resources Development Institute, Bapatla, Guntur, Andhra Pradesh during August 29-31(Mon-Wed), 2016.
- Delivered an Expert lecture on the Topic **“Web Security Vulnerabilities & Best Practices”** at Three-day Online Program on **“Web Development & its Security”** held at Andhra Pradesh Human Resources Development Institute, Bapatla, Guntur, Andhra Pradesh during October 6-8(Tue-Thurs), 2020.
- Delivered an Expert lecture on the Topic **“Common Methods of Threat Mitigation”** at Three-day Online Program on **“Web Development & its Security”** held at Andhra Pradesh Human Resources Development Institute, Bapatla, Guntur, Andhra Pradesh during October 6-8(Tue-Thurs), 2020.
- Devised the curricula and syllabus of **B. Tech. Computer Science & Engineering** and submitted the proposals to Kakatiya University,

Warangal for approval.

- Devised the curricula and syllabus of **B. Tech. Information Technology and submitted** the proposals to Kakatiya University, Warangal for approval.
- Devised the curricula and syllabus of **M.Tech. Software Engineering** and submitted the proposals to Kakatiya University, Warangal for approval.
- Devised the curricula and syllabus of **M.Tech. VLSI & Embedded Systems Design** and submitted the proposals to Kakatiya University, Warangal for approval.
- Devised the curricula and syllabus of **B.Tech. Computer Science & Engineering** for Autonomous Institute namely Lakireddy Balreddy College of Engineering, Mylavaram, Krishna district, 2010.
- Devised the curricula and syllabus of **B.Tech. Information Technology** for Autonomous Institute namely Lakireddy Balreddy College of Engineering, Mylavaram, Krishna dist, 2010.
- Devised the curricula and syllabus of **Master of Computer Applications for Autonomous** Institute namely Lakireddy Balreddy College of Engineering, Mylavaram, Krishna dist, 2010.
- Devised the curricula and syllabus of **B.Tech (Computer Science & Engineering, Information Technology)**, Master of Computer Applications and M.Tech (CSE) for Autonomous Institute namely SR Engineering College, Warangal in 2011.
- Devised the curricula and syllabus of **B.Tech (Computer Science & Engineering, Information Technology)**, **Master of Computer Applications**, **M.Tech (Software Engineering)** and **M.Tech (Computer Science & Engineering)** for Autonomous Institute namely SR Engineering College, Warangal in 2012.
- Devised the curricula and syllabus of **B.Tech (Computer Science & Engineering)**, **M.Tech (Computer Science & Engineering)** and **M.Tech (Software Engineering)** for Autonomous Institute namely SR Engineering College, Warangal in 2013.
- Devised the curricula and syllabus of **B.Tech (Computer Science & Engineering)**, **M.Tech (Computer Science & Engineering)** for Autonomous Institute namely SR Engineering College, Warangal in 2015.
- Devised the curricula and syllabus of **B.Tech (Computer Science & Engineering)**, **M.Tech (Computer Science & Engineering)** for Autonomous Institute namely SR Engineering College, Warangal in 2018.
- Devised the curricula and syllabus of **B.Tech (Computer Science & Engineering)** programs, **M.Tech (Computer Science & Engineering)** for SR University, Warangal in 2020.

References:

- Dr. A.B. Koteswara Rao
Principal,
Gayatri Vidya Parishad College of Engineering
Kommadi, Madhurawada,
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India
- Dr. Dipanwitha Roy Chowdhury
Professor of Computer Science & Engineering
Department of Computer Science & Engineering
Indian Institute of Technology, Kharagpur
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India
- Sri V.Rajanna
Vice President & Regional Head
General Manager – Telecom Technology Business Unit
Tata Consultancy Services (TCS)
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